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Remarkable quality improvement of as-grown monolayer MoS₂ by sulfur vapor pretreatment of SiO₂/Si substrates†

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Monolayer MoS₂ is a direct bandgap semiconductor which is believed to be one of the most promising candidates for optoelectronic devices. Chemical vapor deposition (CVD) is the most popular method to synthesize monolayer MoS₂ with a large area. However, many defects are always found in monolayer MoS₂ grown by CVD, such as sulfur vacancies, which severely degrade the performance of devices. This work demonstrates a concise and effective method for direct growth of high quality monolayer MoS₂ by using SiO₂/Si substrates pretreated with sulfur vapor. The MoS₂ monolayer obtained using this method shows about 20 times PL intensity enhancement and a much narrower PL peak width than that grown on untreated substrates. Detailed characterization studies reveal that MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrates has a much lower density of sulfur vacancies. The synthesis of monolayer MoS₂ with high optical quality and low defect concentration is critical for both fundamental physics studies and potential practical device applications in the atomically thin limit.

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Introduction

Atomically thin two-dimensional transition metal dichalcogenides (TMDCs) have attracted tremendous attention due to their remarkable optical and electrical properties, which make them promising candidates for use in next-generation semiconductors.^{1–8} As a typical member of TMDCs, MoS₂ has been intensively studied. MoS₂ transits from an indirect-to-direct bandgap when the material is thinned from bulk to a monolayer.^{9,10} As a consequence, photoluminescence (PL) arising from the direct bandgap in monolayer MoS₂ broadens its application in optoelectronics such as photovoltaics, photo-

detectors, light sensors and light emitters.^{11–14} Many researchers have focused on the synthesis of the MoS₂ monolayer on a wafer scale and improvement of the optical and electrical performance to speed up its practical applications.^{2,15–19} It has been demonstrated that chemical vapor deposition (CVD) is the most popular fabrication technique for synthesizing large-area monolayer MoS₂ films.^{16–19} However, the structural defects in CVD MoS₂, such as the most commonly seen sulfur vacancies, limit its application in high-performance nanoelectronic and optoelectronic devices.^{20–23} To overcome this challenge, many attempts have been made to repair the chalcogen vacancies in MoS₂, such as post-treatment of MoS₂ with organic reagents which contain sulfur bonded functional groups, oxygen bonding through plasma irradiation on the surface of MoS₂, and so on.^{23–30} Near-unity photoluminescence quantum yield in mechanically exfoliated monolayer MoS₂ has been achieved by treatment with an organic superacid,²⁶ and was assumed to repair the sulfur vacancies in MoS₂.²³ Such a post-treatment process will introduce other unwanted organic impurities on the surface of MoS₂,²⁸ which thus even act as trap sites and further degrade the properties of MoS₂ devices leading to their poor electrical performances.^{27,31} In all, a method to grow monolayer MoS₂ with fewer structural defects is highly desired.

Here, we report a simple but effective method to obtain high quality monolayer MoS₂ through sulfur vapor pretreatment of SiO₂/Si substrates. The differences between monolayer

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MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrates and that grown on untreated SiO₂/Si substrates were systematically investigated. A series of characterization studies including photoluminescence spectroscopy, Raman spectroscopy, X-ray photoelectron spectroscopy (XPS), and electrical transport measurements were carried out to evaluate the quality of the as-grown MoS₂ samples. Through systematical comparison, all the results indicate that monolayer MoS₂ grown on the sulfur vapor pretreated SiO₂/Si substrate has a much lower density of sulfur vacancies. About 20 times enhancement in the PL intensity as well as better electrical performance was observed in monolayer MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrates than that grown on untreated substrates. The strategy of synthesizing monolayer MoS₂ by sulfur vapor pretreatment of SiO₂/Si substrates is not only beneficial to greatly improve the quality of as-grown monolayer MoS₂, but also compatible to the existing mature process of planar semiconductor devices. This pretreatment strategy can avoid the unwanted defects and impurities which may be introduced during post-treating process by organic reagents. Our method demonstrated here will benefit the research community in the synthesis of high quality monolayer MoS₂ and its further application.

Experimental

Sulfur vapor pretreatment of SiO₂/Si substrates

A two-temperature-zone heating furnace with a 2-inch quartz tube was used for both treating the growth substrates in a sulfur vapor environment and synthesizing monolayer MoS₂. The temperature of the two-heating zones can be independently controlled during the whole process. A high purity sulfur slice (Aladdin, 99.9%) and SiO₂/Si substrates (300 nm SiO₂) were separately loaded in a one-end sealed quartz tube and fixed at the center of the corresponding heating zone, which could effectively increase the concentration and pressure of sulfur vapor during the pretreatment of the substrates. Next, the sulfur source was heated to 200 °C to produce sulfur vapor. The SiO₂/Si substrates were pretreated at 200 °C, 400 °C, 500 °C, 600 °C, and 700 °C individually for an hour in the sulfur vapor environment. After the pretreated SiO₂/Si substrates were cooled down to room temperature, several droplets of perylene-3,4,9,10-tetracarboxylic acid tetrapotassium salt (PTAS) solution were dropped on the pretreated SiO₂/Si substrates. The MoS₂ samples were synthesized by using home-made CVD equipment with MoO₃ powder and sublimed sulfur as the growth source, and the growth temperature was set at 680 °C using high purity Ar as the carrier gas under atmospheric pressure. The details about growth recipe can be found in our previous publication.¹⁹

Characterization studies

The optical images were acquired using optical microscopy (Keyence digital microscope VHX-600). Raman mapping and PL mapping were carried out using a confocal Raman microscope (WITec Alpha 300R) with a 532 nm laser for the exci-

tation. The Raman signal was collected using a 100× (Zeiss, N.A. = 0.9) objective lens after being dispersed by optical grating (1800 lines per mm); the same measurement parameters were chosen for the PL measurement, but 150 lines per mm was chosen. To avoid the heating effect, the power of the excitation laser was kept at 0.5 mW. All the Raman and PL measurements except temperature dependent PL were carried out at room temperature in air. The temperature dependent PL spectra were recorded using confocal micro-Raman spectroscopy (Renishaw) with a 514 nm laser for the excitation through a 50× (Leica, N.A. = 0.5) objective lens after being dispersed by optical grating (1800 lines per mm) in a Linkam thermal stage. The extended grating scan type was chosen to obtain PL spectra in the selected range, and the laser power was kept below 0.1 mW to ultimately avoid the heating effect. The XPS spectra were recorded using a ThermoFisher ESCALAB 250Xi with an Al Kα X-ray source under a pressure of 2.2×10^{-10} mbar.

TFSI treatment of MoS₂

The treatment process of our CVD grown monolayer MoS₂ with bis(trifluoromethane)sulfonimide (TFSI) is as follows: 100 mg TFSI (Aladdin) was dissolved in 500 mL dichloromethane (CH₂Cl₂) to make a 0.2 mg ml⁻¹ TFSI solution, and then the monolayer MoS₂ samples grown on SiO₂/Si substrates were immersed in 10 ml TFSI solution in a tightly closed vial, and then the vial was placed on a heated hotplate at 100 °C for 10 min. After the vial was cooled down to room temperature, the samples were subsequently taken out and placed on the hotplate (100 °C) for 5 min and finally cooled down to room temperature.

Fabrication and testing of MoS₂ FET devices

Two electrodes (source/drain) were patterned by photolithography using a laser direct writing machine (MicroWriter ML③3) without a photomask. 5 nm Cr and 50 nm Au were deposited by thermal evaporation (Kurt J. Lesker, NANO 36) as the metal electrodes for making MoS₂ field effect transistors (FETs). Then the devices were annealed under 190 sccm Ar and 10 sccm H₂ at 200 °C for 2 h to remove the organic residuals and improve the contact between the MoS₂ sample and metal electrodes. All the electrical measurements were carried out using an Agilent Technologies B1500A semiconductor device analyzer under dark conditions at room temperature in an ambient atmosphere.

Results and discussion

Fig. 1(a) shows the schematic of the experimental setup for pretreating SiO₂/Si substrates in a sulfur vapor environment. The extremely enhanced PL of monolayer MoS₂ can be found when the SiO₂/Si substrates were pretreated with sulfur vapor at 600 °C (if not specified, all the pretreatments of the SiO₂/Si substrates are carried out at 600 °C in a sulfur vapor environment; the details are discussed later). As shown in Fig. 1(b)

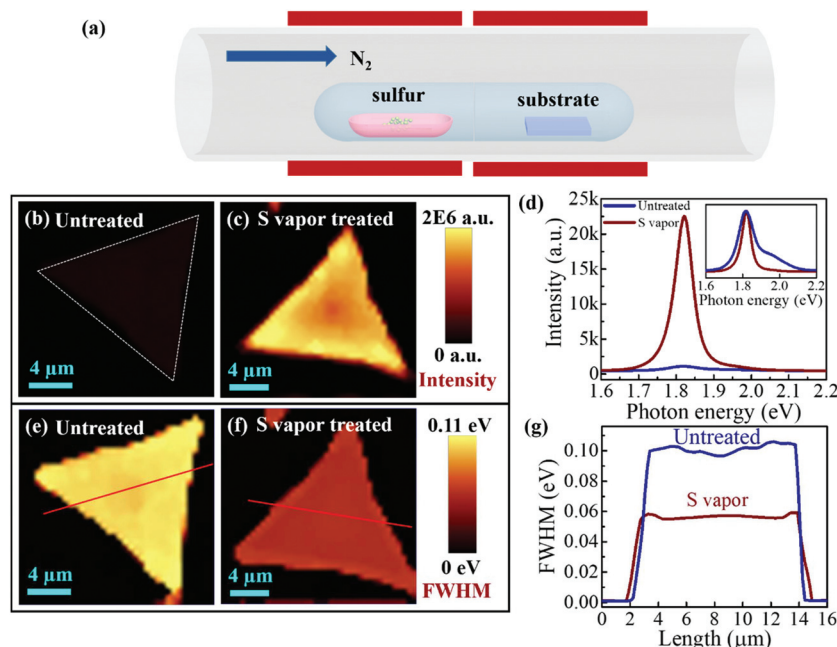


Fig. 1 (a) Schematic of the experimental setup for pretreating SiO₂/Si substrates in sulfur vapor. (b and c) PL peak intensity mapping of CVD grown monolayer MoS₂ on untreated (b) and sulfur vapor pretreated SiO₂/Si substrates (c). (d) Typical PL spectra of MoS₂ grown on untreated (blue line) and sulfur vapor pretreated SiO₂/Si substrates (red line). The inset shows the normalized PL spectra. (e and f) PL mapping of full width at half maximum (FWHM) of MoS₂ domains corresponding to (b and c), respectively. (g) PL peak linewidth corresponding to the red line in (e and f).

and (c), the PL intensity of the whole monolayer MoS₂ flake grown on the sulfur pretreated SiO₂/Si substrate was about 20 times higher than that grown on the untreated SiO₂/Si substrate, which can be due to the much more defect-mediated nonradiative recombination existing in MoS₂ grown on the untreated substrate.³² The representative extracted PL spectra from the PL mapping of monolayer MoS₂ grown on sulfur vapor pretreated and untreated substrates are shown in Fig. 1(d). A narrower linewidth is observed in MoS₂ grown on sulfur vapor pretreated substrates as seen from the inset of Fig. 1(d). Such an extremely enhanced PL with a narrow linewidth of monolayer MoS₂ grown on the sulfur vapor pretreated substrate indicates the much better optical quality of monolayer MoS₂ grown on the sulfur vapor pretreated substrate than that grown on the untreated substrate.^{23,25,26} Due to the spin-orbit splitting of the highest valence band, there are two direct-gap emission peaks called A-excitons and B-excitons in the PL spectrum of monolayer MoS₂.⁹ It has been reported recently that the A- and B-exciton photoluminescence intensity ratio can be used to measure the sample quality for transition metal dichalcogenide monolayers such as MoS₂, MoSe₂, WS₂, and WSe₂.³³ The A- and B-excitons are easily seen in MoS₂ grown on the untreated SiO₂/Si substrate, whereas the B-excitons in MoS₂ grown on the sulfur vapor pretreated substrate are not so easy to discern because of the remarkably high intensity of A-excitons. Upon further analysis and re-plotting the same PL spectra, as shown in Fig. 1(d), with intensity on a log-scale, the B-excitons become discernible, as shown in Fig. S1.† The intensity ratios of B-excitons *versus* A-excitons

(B/A) are about 1/2 and 1/20 in MoS₂ grown on untreated and sulfur vapor pretreated substrates, respectively. Therefore, we can qualitatively assess the quality of MoS₂ samples through the B/A ratio; a low B/A ratio in MoS₂ grown on the sulfur vapor pretreated SiO₂/Si substrate indicates low defect density and thus corresponds to high crystalline quality. For comparison, we also recorded the PL spectra and mapping of mechanically exfoliated monolayer MoS₂ (shown in Fig. S2†). The PL intensity of monolayer MoS₂ grown on untreated substrates has the same magnitude as that of the exfoliated monolayer MoS₂, which means that the PL intensity of monolayer MoS₂ grown on pretreated substrates is much stronger than that of exfoliated monolayer MoS₂, that is, the optical quality of monolayer MoS₂ grown on the pretreated substrate is much better than that of mechanically exfoliated monolayer MoS₂. We show the PL mapping of full width at half maximum (FWHM) and the corresponding linewidth in Fig. 1(e–g). It can be found that the FWHM of MoS₂ grown on sulfur vapor pretreated substrates is only a half of that of MoS₂ grown on the untreated substrate, which means less structural disorder or defects and better crystallinity in the former. The PL peak width of as-grown monolayer MoS₂ (50–60 meV) on the sulfur vapor pretreated substrate is comparable to that of the CVD grown sample on h-BN and free-standing MoS₂.^{9,34–36} These characteristics of PL suggest that the MoS₂ grown on the sulfur vapor pretreated SiO₂/Si substrate is less perturbed due to its high quality.^{36,37}

To further evaluate the quality of MoS₂ flakes, we carried out temperature dependent PL characterization. As shown in

Fig. S3,[†] for the monolayer MoS₂ grown on untreated substrates, a shoulder peak below the A exciton starts appearing when the temperature is decreased to 140 K, and the relative intensity of this peak increases with a further decrease in temperature, whereas for the monolayer MoS₂ sample grown on the sulfur vapor pretreated substrate, this shoulder peak only appears when the temperature is decreased to 100 K. It is obvious that the relative intensity of this shoulder peak to A exciton is much weaker than that of MoS₂ on the untreated substrate at the same temperature. This shoulder peak is denoted as localized states (L peak) probably originating from defects, impurities, and disorders, and is also found in other TMDs such as WS₂.^{38,39} It was believed that the L peak is related to defect-bound excitons such as sulfur vacancies in MoS₂ and WS₂.^{38–41} Combining the extremely enhanced PL and the relatively weaker L peak that emerged at low temperature, we assume that a much lower density of sulfur vacancies exists in MoS₂ grown on the sulfur vapor pretreated substrate than on the untreated substrate.

Raman characterization was performed to further investigate the crystal quality of the MoS₂ flakes grown on the SiO₂/Si substrate with and without sulfur vapor pretreatment, as shown in Fig. 2. The typical Raman spectrum of MoS₂ grown on the untreated substrate, shown in the top panel of Fig. 2(a), exhibits two characteristic peaks at 383.6 cm⁻¹ and 403.4 cm⁻¹ corresponding to the in-plane vibration E_{2g}¹ mode and out-of-plane vibration A_{1g} mode. The peak difference between the two modes is 19.8 cm⁻¹, indicating the monolayer characteristic of the CVD synthesized MoS₂.^{5,21–23} It is known that the in-plane E_{2g}¹ mode of MoS₂ is sensitive to strain and the out-of-plane A_{1g} mode is related to doping.⁴² For the Raman spectrum of monolayer MoS₂ grown on the sulfur vapor pretreated substrate in the lower panel of Fig. 2(a), the in-plane E_{2g}¹ mode shows a 0.7 cm⁻¹ shift towards lower wavenumbers compared with MoS₂ grown on the untreated substrate. This obvious red shift of the E_{2g}¹ mode is assumed to be due to the

reduced sulfur vacancies, which lead to the release of compress strain.^{23,43} The blue shift in the A_{1g} mode suggests that the reduced n-doping is induced by the much lower density of sulfur vacancy defects.^{23,42} The red shift of the E_{2g}¹ mode and the blue shift of the A_{1g} mode are observed in the whole flake of monolayer MoS₂ grown on the sulfur vapor pretreated substrate from the Raman peak position mapping, as shown in Fig. 2(b–e); the statistic peak positions of the E_{2g}¹ mode and A_{1g} mode are also shown in Fig. S4.[†] In addition, we find that the Raman mapping of the FWHM of the E_{2g}¹ mode does not show an obvious difference in MoS₂ grown on both substrates, whereas the FWHM of the A_{1g} mode in monolayer MoS₂ grown on the sulfur vapor pretreated substrate is slightly narrower than that grown on the untreated substrate, as shown in Fig. S5.[†] The FWHM value of the A_{1g} mode in monolayer MoS₂ grown on the sulfur vapor pretreated substrate is about 4.5 cm⁻¹, which is even narrower compared to that of the MoS₂ grown on h-BN,⁴⁴ indicating the reduced electron-phonon coupling related to structural disorder and doping in MoS₂ grown on the sulfur vapor pretreated substrate.⁴² Furthermore, we carried out XPS measurements on MoS₂ grown on sulfur vapor pretreated and untreated substrates. As shown in Fig. S6,[†] the Mo 3d core level spectra were fitted with three sets of double peaks corresponding to three kinds of Mo bonded compound states. The purple curve with double peaks located at 236.1 and 232.9 eV represents Mo⁶⁺ 3d_{3/2} and 3d_{5/2} doublets of MoO₃ or MoO_x, respectively, which may be attributed to the residual growth source that was incompletely sulfurized.⁴⁵ The Mo⁴⁺ spectra were deconvoluted into two components, for the green doublets corresponding to Mo⁴⁺ 3d_{3/2} and 3d_{5/2} in stoichiometric intrinsic MoS₂ (i-MoS₂), whereas the blue doublets located at a slightly lower binding energy than green doublets correspond to Mo⁴⁺ 3d_{3/2} and 3d_{5/2} in defective MoS₂ (d-MoS₂) with sulfur vacancies.^{23,29,45} Based on this point, from the deconvoluted component spectra in Mo⁴⁺, it is found that the proportion of d-MoS₂ becomes much less

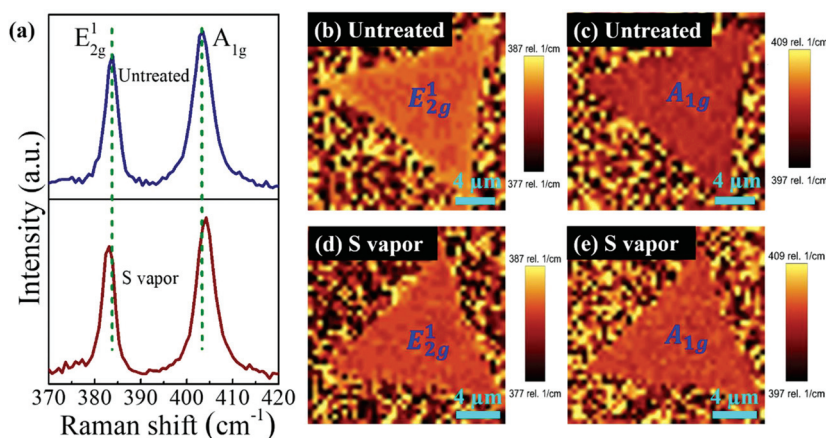


Fig. 2 (a) Typical Raman spectra of monolayer MoS₂ grown on the untreated SiO₂/Si substrate (blue line) and the sulfur vapor pretreated SiO₂/Si substrate (red line). (b and c) Raman peak position mapping of the E_{2g}¹ mode (b) and A_{1g} mode (c) of monolayer MoS₂ grown on the untreated SiO₂/Si substrate. (d, e) Raman peak position mapping of the E_{2g}¹ mode (d) and A_{1g} mode (e) of monolayer MoS₂ grown on the sulfur vapor pretreated SiO₂/Si substrate.

prominent in MoS₂ grown on sulfur vapor pretreated substrates, suggesting reduced sulfur vacancies, further supporting our previous assumption.

To understand the mechanism of reduced sulfur vacancies in as-grown monolayer MoS₂ by sulfur vapor pretreatment of SiO₂/Si substrates, XPS measurements were performed on the SiO₂/Si substrates pretreated with sulfur vapor at different temperatures. Fig. 3(a) and (b) show the XPS spectra of the Si 2s and S 2p core level of SiO₂/Si substrates pretreated with sulfur vapor at different temperatures, respectively. It can be seen that below 400 °C, there was no XPS signal of the S element. When the treating temperature exceeds 500 °C, the content of the S element increases with an increase in the treating temperature. It should be noted that before the XPS measurements, the sulfur vapor pretreated substrates were loaded under a pressure of 2.2×10^{-10} mbar for more than an hour in order to eliminate the elemental sulfur. So the S 2p core level peak of XPS spectra is ascribed to sulfur related chemical bonds with the terminal atoms on the surface of SiO₂/Si substrates. As there are abundant oxygen and silicon dangling bonds on the substrate, sulfur related bonds are the complex of S–O and S–Si bonds, which are similar to the sulfur-terminated chemical bonds on the Si surface.^{46–49} From the statistics of the PL spectra of MoS₂ grown on sulfur vapor pretreated substrates with different treating temperatures, it is interestingly found that the PL intensity becomes saturated when the treating temperature is beyond 600 °C. The above phenomenon can be explained as follows: at low temperature,

no sulfur related chemical bond has been formed yet, so there was almost no obvious difference in the PL intensity when MoS₂ was grown on sulfur vapor pretreated substrates below 400 °C. With a further increase in the temperature beyond 500 °C, sulfur related chemical bonds started to form on the surface of the SiO₂/Si substrates. The growth period of MoS₂ involves the dissociation of the sulfur related chemical bond on the surface of the substrates and the formation of the Mo–S bond at the sulfur vacancies, which is similar to the repair process in the chemical treatment of MoS₂.^{23,24,28,29} With a further increase in the treating temperature beyond 600 °C, the PL intensity of MoS₂ grown on the substrates becomes slightly lower, which may be due to the substitution of the Mo atoms by excess sulfur atoms, leading to other kinds of defects such as antisite defects in the grown MoS₂.²⁰

Low quantum yields (QYs) in monolayer TMDCs limit their practical application in optoelectronics, which are mainly due to the structural defects in TMDCs. Among all the types of structural defects, chalcogen vacancies are one of the most commonly seen structural defects due to the lowest formation energy.^{20,23,41} Eliminating the structural defects such as sulfur vacancies in TMDCs to increase the optical and electrical performance has been widely investigated.^{23–30} It is believed that the sulfur vacancies in MoS₂ can be repaired by the bis(trifluoromethane)sulfonimide (TFSI) treatment.^{23,25,26} Fig. 4 shows the PL mapping and extracted single spectrum of MoS₂ grown on untreated and sulfur vapor pretreated substrates before and after TFSI treatment. As for the MoS₂ grown on

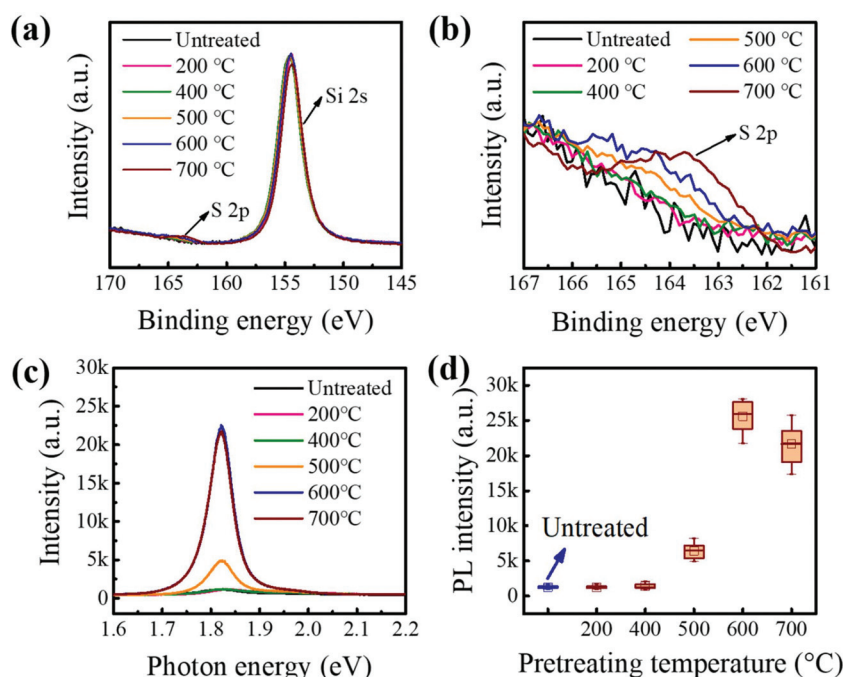


Fig. 3 Investigation of the SiO₂/Si substrates treated under different conditions and MoS₂ subsequently obtained. (a) XPS spectra of Si 2s and S 2p core level peaks of SiO₂/Si substrates pretreated with sulfur vapor at different temperatures. (b) The enlarged S 2p core level spectra of these SiO₂/Si substrates. (c) Typical PL spectra of MoS₂ grown on SiO₂/Si substrates treated with S vapor at different temperatures. (d) Statistics of PL intensities of MoS₂ grown on these SiO₂/Si substrates.

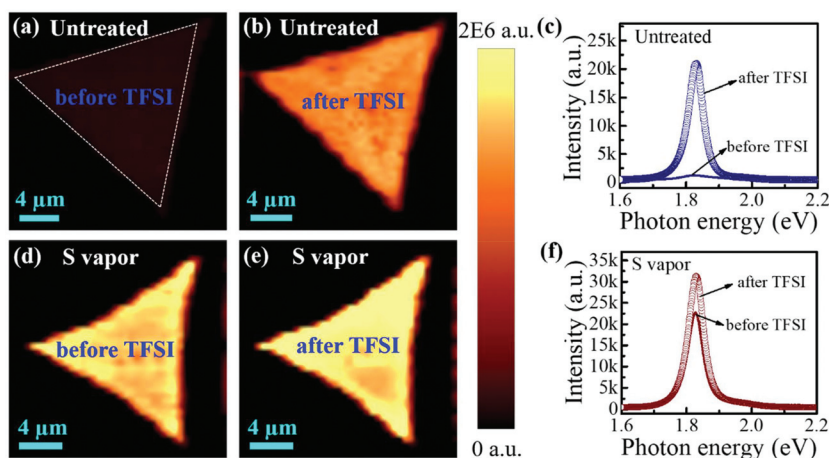


Fig. 4 (a and b) PL peak intensity mapping of monolayer MoS₂ grown on the untreated SiO₂/Si substrate before (a) and after TFSI treatment (b). (c) Typical extracted PL spectra from the mapping area of (a, b). (d, e) PL peak intensity mapping of monolayer MoS₂ grown on the sulfur vapor pretreated SiO₂/Si substrate before (d) and after TFSI treatment (e). (f) Typical extracted PL spectra from the mapping area of (d, e).

untreated substrates, the PL intensity increased about 10 times after TFSI treatment, which means that many sulfur vacancies were repaired by the extrinsic sulfur atoms dissociated from

TFSI.²³ And the linewidth of the PL spectrum becomes much narrower, which was assumed to be due to the decreased structural disorder. However, for the MoS₂ grown on sulfur vapor

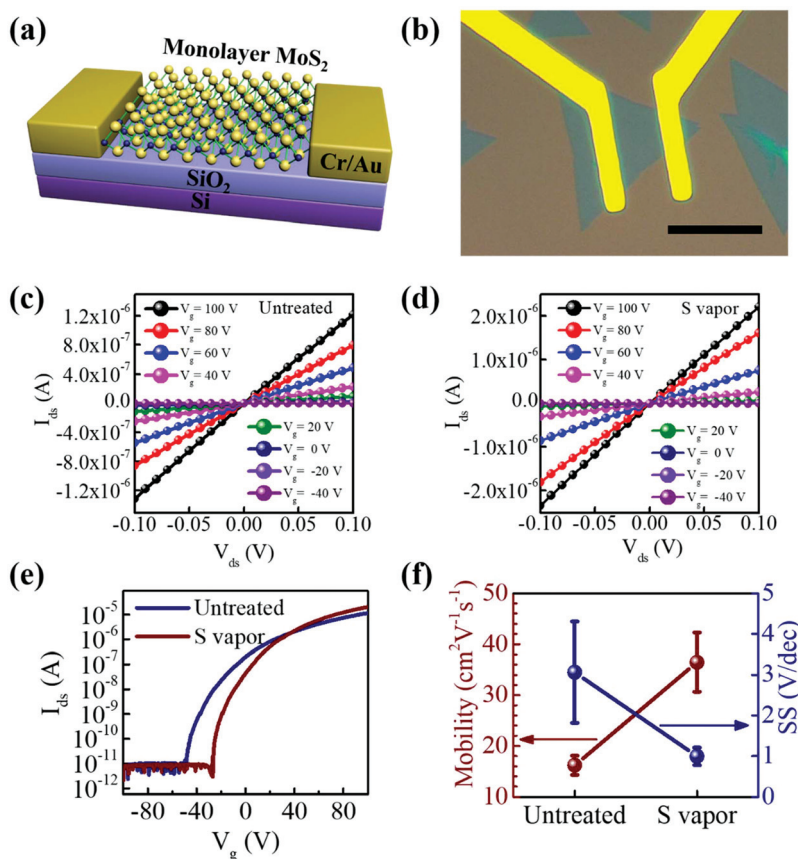


Fig. 5 (a) Three-dimensional schematic of a monolayer MoS₂ transistor. (b) Optical image of a MoS₂ FET device. Scale bar, 20 μm. (c) Output characteristics of monolayer MoS₂ grown on untreated SiO₂/Si substrate. (d) Output characteristics of monolayer MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrate. (e) Typical transfer characteristics of monolayer MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrate (red line) and untreated SiO₂/Si substrate (blue line). (f) Statistical mobility and subthreshold swing of MoS₂ FET devices.

pretreated substrates, only a little enhancement in PL can be achieved, which indicates less defect-mediated nonradiative recombination in the as-grown MoS₂ before TFSI treatment. From TFSI repaired sulfur vacancy defects, the elimination of sulfur vacancy defects by TFSI is more obvious in MoS₂ grown on untreated substrates. We conclude that the density of sulfur vacancies is much lower in MoS₂ grown on sulfur vapor pretreated substrates.

Finally, we performed electrical characterization of the monolayer MoS₂ samples. Fig. 5(a) and (b) show the schematics and typical optical image of the MoS₂ transistor, respectively. Back-gated FET devices on the individual MoS₂ flakes were directly fabricated on the growth substrates without a further transfer process. The linear current-voltage (I_{ds} - V_{ds}) curves of MoS₂ flakes grown on both substrates indicate that the contact between MoS₂ and metal electrode is ohmic, as illustrated in Fig. 5(c) and (d). The transfer curves of MoS₂ FET devices in Fig. 5(e) show good n-type semiconducting behavior, which is consistent with earlier literature reports.^{2,3,16–18} The on/off current ratio of the MoS₂ FET is about 10^6 – 10^7 . The mobility can be calculated based on the formula:

$$\mu = \frac{L}{W \times C_g \times V_{ds}} \times \frac{dI_{ds}}{dV_g}, \quad (1)$$

where L and W are the channel length and width, respectively, and C_g is the capacitance ($C_g = 1.15 \times 10^{-8}$ F cm⁻²) for 300 nm SiO₂. Multiple MoS₂ FET devices were tested, and the statistical results are shown in Fig. 5(f). The mobility of MoS₂ grown on untreated substrates ranged from 13–18.8 cm² V⁻¹ s⁻¹, whereas the mobility of MoS₂ grown on sulfur vapor pretreated substrates can be up to 40 cm² V⁻¹ s⁻¹ (as seen from Fig. 5(e), red line), which exceeds twice the value of that grown on untreated substrates. The mobility of monolayer MoS₂ grown on the sulfur vapor pretreated substrate is superior to most reported values of mechanically exfoliated monolayer MoS₂ and CVD grown monolayer MoS₂ without post-treatment (as shown in Fig. S7†). It should be noted that the mobility of up to 81 cm² V⁻¹ s⁻¹ of MoS₂ is on the precondition that both sides of MoS₂ should be treated with sulfur-containing molecules.²⁴ On the one hand, it is found that the statistic sub-threshold swing (SS) of MoS₂ grown on untreated substrates is more than twice larger than that grown on sulfur vapor pretreated substrates, which means that the latter has reduced interface trap states and better crystal quality.²⁶ On the other hand, we found that the threshold voltage has an obvious negative shift in MoS₂ grown on untreated substrates compared with MoS₂ grown on sulfur vapor pretreated substrates, which means that the much heavier n-doping resulted from the higher density of sulfur vacancies in MoS₂ grown on untreated substrates.

Conclusions

In summary, we demonstrate a simple and effective strategy to synthesize high quality monolayer MoS₂ by pretreating SiO₂/Si

substrates with sulfur vapor. The optical responses of monolayer MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrates are remarkably improved compared with those of monolayer MoS₂ grown on untreated SiO₂/Si substrates. Raman characterization and TFSI treatment confirm that MoS₂ grown on sulfur vapor pretreated substrates has a much lower density of sulfur vacancies. The carrier mobility of MoS₂ grown on sulfur vapor pretreated SiO₂/Si substrates reaches up to 40 cm² (V s)⁻¹ even at ambient temperature. Our work boosts the research on MoS₂ both in the fundamental studies and its further application in optoelectronics.

Author contributions

Chunxiao Cong directed the research work. Chunxiao Cong and Peng Yang conceived and designed the experiments. Peng Yang fabricated the MoS₂ samples. Peng Yang and Yabing Shan performed the Raman and PL measurements. Peng Yang and Jing Chen fabricated the MoS₂ devices and performed the electrical performance measurements. Chunxiao Cong, Peng Yang, Haomin Wang, and Zhi-Jun Qiu analysed the data. Chunxiao Cong and Peng Yang co-wrote the manuscript. All authors discussed the results and commented on the manuscript.

Conflicts of interest

There are no conflicts to declare.

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