



# **Processes of the Reliability and Degradation Mechanism of High-Power Semiconductor Lasers**

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**Abstract:** High-power semiconductor lasers have attracted widespread attention because of their small size, easy modulation, and high conversion efficiency. They play an important role in national economic construction and national defense construction, including free-space communication; industrial processing; and the medical, aerospace, and military fields, as well as other fields. The reliability of high-power semiconductor lasers is the key point of the application system. Higher reliability is sought in the military defense and aerospace fields in particular. Reliability testing and failure analysis help to improve the performance of high-power semiconductor lasers. This article provides a basis for understanding the reliability issues of semiconductor lasers across the whole supply chain. Firstly, it explains the failure modes and causes of failure in high-power semiconductor lasers; this article also summarizes the principles and application status of accelerated aging experiments and lifetime evaluation; it also introduces common techniques used for high-power semiconductor laser failure analysis, such as the electron beam-induced current (EBIC) technique and the optical beam-induced current (OBIC) technique, etc. Finally, methods used to improve the reliability of high-power semiconductor lasers are proposed in terms of the preparation process, reliability screening, and method application.

**Keywords:** high-power semiconductor laser; failure mechanisms; accelerated aging test; failure analysis techniques

# 1. Introduction

High-power semiconductor lasers have the advantages of small size, light weight, high electro-optical conversion efficiency, and easy monolithic integration, and are widely used in free-space communication; industrial processing; and the medical, aerospace, and military fields, as well as in other fields. High power characteristics and the long-term stability of the laser's wavelength and bandwidth are important prerequisites for the broad application of semiconductor lasers. Generally speaking, levels above 100 mW for narrow-stripe, single-mode devices and levels above 1 W for all other single- and multi-emitter



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**Copyright:** © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). lasers can be considered to be high power [1]. The reliability of high-power semiconductor lasers is limited by the optical power density at the output facet, heat dissipation, and the current density in the semiconductor. Understanding the reliability and failure mechanisms of high-power semiconductor lasers is essential for the development of high-performance and highly reliable application systems.

Research on the degradation mechanisms of semiconductor lasers dates back to the 1960s. In 1966, internal self-damage in gallium arsenide lasers was investigated by D.P. Cooper et al. from Services Electronics Research Lab. Baldock, Herts, UK. They suggested that the damage occurred as a result of the interaction between a critical high flux density and some randomly distributed structural properties such as defects or diffusion irregularities [2]. In 1967, H. Kressel and H. Mierop from RCA Laboratories postulated that the catastrophic damage in a GaAs injection laser resulted from the effects generated by stimulated Brillouin emissions [3].

In the mid-1970s, the Nippon Telegraph and Telephone Corporation (NTT) and the Nippon Electric Corporation (NEC) in Japan formed two research groups for collaboration in the study of the reliability of semiconductor lasers for optical communications [4]. However, for the next two decades, reliability research studies were limited by the technology, and the degradation failure phenomenon was not fully understood at the atomic level [5,6]. In the 21st century, IBM Zurich Research Laboratory (Switzerland) [7–9], Sumitomo Electric Industries, Ltd. (Japan) [10], Agilent (USA) [11], and the American Aerospace Corporation [12] have conducted systematic studies on the reliability and degradation of semiconductor lasers. The degradation mechanisms are gradually being addressed.

Analysis of the failure mechanism of high-power semiconductor lasers is an important basis for studying their reliability. In this paper, the failure mechanisms of high-power semiconductor lasers are introduced in detail, including three failure modes and the causes of performance degradation, such as internal degradation, mirror facet degradation, electrode degradation, packaging-related degradation, and the influence of environmental factors. The principles of accelerated aging experiments in high-power semiconductor lasers and their applications are also summarized. Failure analysis techniques such as the electron beam-induced current (EBIC) technique, the optical beam-induced current (OBIC) technique, the thermally induced voltage alteration (TIVA) technique, electroluminescence (EL), microphotoluminescence mapping (µ-PL), emission microscopy (EMMI), cathodoluminescence (CL), electron channeling contrast imaging (ECCI), transmission electron microscopy (TEM), and Raman are introduced in detail. The advantages and limitations of each technique are compared. Finally, methods by which to improve the reliability of high-power semiconductor lasers are proposed in terms of the preparation process, reliability screening, and method application. It is hoped that this study can provide a reference for research on the failure mechanisms of high-power semiconductor lasers, as well as improvements to their reliability.

# 2. Failure Mechanism

The performance of semiconductor lasers decreases with an increasing operating time. The most obvious manifestation of this is the decrease in output power and electro-optical conversion efficiency of the semiconductor laser at a constant drive current. In addition, catastrophic damage to the laser leads to a sudden drop in optical power [13]. Therefore, an increase in the drive current is required to prevent the degradation of the laser during constant power operation. The change in output power during laser degradation is mainly caused by the decrease in the lifetime of the injected carriers and the increase in internal optical losses. The mathematical model of the optical power output of a semiconductor laser is as follows [1]:

$$P_{out} = \eta_e (I - I_{th}) \frac{\hbar \omega}{a} \tag{1}$$

$$\eta_e = \eta_i \frac{\alpha_m}{\alpha_m + \alpha_i} \tag{2}$$

$$\alpha_m = \frac{1}{2L} ln \frac{1}{R_1 R_2} \tag{3}$$

where  $P_{out}$  is the optical output power,  $\eta_e$  is the external differential quantum efficiency, I is the drive current,  $I_{th}$  is the threshold current, q is the electron charge, and  $\hbar\omega$  is the energy quantum.  $\eta_i$  is the internal differential quantum efficiency;  $\alpha_i$  is the internal loss, mainly caused by the free-carrier absorption of the waveguide material and the scattering loss due to the roughness of the optical waveguide layer; and  $\alpha_m$  is the mirror loss. L is the cavity length,  $R_1$  and  $R_2$  are the front cavity mirror reflectivity and the rear cavity mirror reflectivity, respectively. Then, the root cause parameters directly affecting the decrease in output power are the increase in the threshold current and the decrease in the slope efficiency or external differential quantum efficiency.

# 2.1. Failure Mode

In most operating systems, semiconductor lasers usually operate at a constant output power and lasing wavelength. Therefore, when degradation occurs, variations in the drive current show different patterns, as shown in Figure 1.



Figure 1. Failure mode of the laser diode at constant output power.

The failure modes of high-power semiconductor lasers are similar to those of ordinary semiconductor lasers. According to the relationship between drive current and lifetime, the failure modes of high-power semiconductor lasers are manifested in the following three forms: rapid failure, gradual failure, and sudden failure after gradual failure [14].

# 2.1.1. Rapid Failure

Rapid failure is usually observed within the first hundred hours of operation of a semiconductor laser. It manifests itself as a rapid drop in the output power and a rapid increase in the threshold current. Rapid failure is associated with defects present within the laser. These defects are introduced during the epitaxial growth of the wafer or the fabrication of the chip. The defects (such as the precipitation of impurity atoms and dislocations) are accelerated by non-radiative recombination processes; there is also vibration energy generated by recombination-enhanced dislocation climb or glide (REDC or REDG) [15,16]. As defects propagate in the diode structure, dislocation networks form and grow in the crystal structure, and these defects can destroy the active region of the semiconductor laser and make the laser ineffective. The types and formation mechanism of defects in degraded high-power semiconductor lasers are shown in Table 1 below.

Lasing Wavelength	Semiconductor Lasers	Defect Types	Mechanism
440–450 nm	InGaN/GaN QW lasers	<11-20> a-type dislocations [17]	Climb mechanism involving point defect
808 nm	AlGaAs/GaAs QW-SCH lasers	<110> DLDs Dislocations dipoles [18]	Gliding mechanism
980 nm	InGaAs/AlGaAs QW lasers	<100> DLDs Dislocation dipoles or climbed dipoles [18] <1-10> DLDs Edge dislocation dipoles	Climb mechanism involving point defect Gliding mechanism not involving point defect
1300 nm	InGaAsP/InP QW lasers	<100> DLDs Dislocation dipoles [19]	Climb mechanism involving point defect
1550 nm	InGaAs/InP QW lasers	V-shaped defects DLDs Misfit dislocations [20]	Climb mechanism

Table 1. Defect types in degraded high-power semiconductor lasers.

To eliminate rapid failure, it is necessary to apply low-defect or defect-free substrates to eliminate dark-line defects (DLDs) or dark-spot defects (DSDs) formed by the REDC or REDG mechanism. In addition, the selection of lattice-matched epitaxial films to reduce internal stresses in the laser structure and the selection of a support solder to reduce external stress during bonding are useful methods. Last but not least, avoiding defect formation during the epitaxial growth and fabrication processes is very important for reducing the probability of rapid failure.

# 2.1.2. Gradual Failure

This failure mode manifests itself in several forms, including a gradual and slow longterm decrease in the optical output power, and an increase in the threshold current with an increasing operation or aging time. To reach a certain output power, the drive current of the semiconductor laser needs to be increased. During this degradation process, the slope efficiency of the laser remains constant, and, for a constant drive current, the output power of the laser experiencing this failure mode can be seen to decrease over time. In general, the optical power of the laser must be recovered at the cost of a higher drive current [21]. The gradual failure mode often determines the maximum operating lifetime of semiconductor lasers. The mechanism of gradual failure is related to the enhancement of internal stress and the increase in non-radiative recombination centers in the active region. This failure is always associated with the formation of DSDs, caused by the growth of dislocation networks, and internal stresses, which are caused by defect clusters or micro-dislocation loops in the active region of high-power semiconductor lasers [4].

To avoid the gradual failure of semiconductor lasers, it is important to reduce the formation of non-radiative deep-level defect centers during epitaxial growth and device fabrication [22]. For example, a diffusion barrier layer is created during the preparation of metal electrodes to eliminate the inter-diffusion of metals and semiconductor materials.

Elimination of internal and external stresses around the active region, such as the selection of an appropriate strained quantum well (QW) structure, is an effective way to avoid long-term degradation. When the thickness of the QW layer is designed below a certain level, no misfit dislocation is formed due to strain relaxation. A heat sink material with a coefficient of thermal expansion that matches that of the semiconductor laser chip should be selected.

# 2.1.3. Sudden Failure after Gradual Failure

Sudden failure after gradual failure is manifested by a semiconductor laser that has been operating for a long period under normal operating conditions, but suddenly has no light intensity output. This is the most serious failure mode in high-power semiconductor lasers. Sudden failure after gradual failure in most devices is associated with DLDs [23]. This degradation mode is closely related to electrical, optical, and thermal overloads and also limits the maximum power of high-power semiconductor lasers. In addition, catastrophic optical mirror damage (COMD) and electrode, solder, or heat sink degradation may also lead to sudden failure after gradual failure [24].

Effective cavity surface passivation methods, vacuum cleavage, and coating techniques or the fabrication of non-absorbing mirror structures, such as the use of QW intermixing technologies, are applied to prevent the COMD phenomenon. Appropriate composite heat sinks with high thermal conductivity and less thermal expansion stress can effectively improve the reliability of semiconductor lasers due to their good matching of thermal expansion efficiency to the chip.

### 2.2. Reasons for Degradation

As with many other types of electronic devices, the reliability and lifetime of highpower semiconductor lasers depend heavily on their operating conditions, the suitability of the epitaxial materials, and device structure. The high optical power density at the output facet, heat accumulation, and the large current density in the semiconductor have a significant influence on the reliability of high-power semiconductor lasers. Depending on the type and location of the degradation in semiconductor lasers, degradation can be classified as internal, mirror facet, electrode, degradation related to packaging, and degradation caused by environmental factors.

# 2.2.1. Internal Degradation

As the operating time increases, local gain saturation or the spatial hole burning effect lead to the formation of filamentation, self-focusing, or thermal lensing in the chip. Optically induced heating enhances defects, which are introduced by the substrate, epitaxial growth, or fabrication processes migrating to the active region. The non-radiative recombination rate of the carrier is accelerated [12]. The energy generated by the non-radiative recombination process is converted into an Auger recombination or lattice vibrations, leading to recombination-enhanced defect reactions (REDRs). The propagation of defects through the REDC and REDG mechanisms leads to the formation of dislocation networks known as DLDs [4]. The REDR process is further enhanced by the strong optically induced heating phenomenon. The slow propagation of internal defects within the epitaxial material leads to an increase in non-radiative recombination centers in the active region, accompanied by a decrease in the electro-optical conversion efficiency and the output power. As a result, degradation of the semiconductor laser occurs. Internal degradation also includes DLDs and DSDs, which often lead to the rapid failure of semiconductor lasers. Figure 2 shows a TEM image of a typical DLD observed in a rapidly degraded GaAlAs/GaAs double-heterojunction laser [25]. DLD can also cause catastrophic optical bulk damage (COBD), leading to the rapid failure of the laser [26]. COBD refers to the catastrophic damage occurring inside the laser, which is different from that on the mirror facet.

# 2.2.2. Mirror Facet Degradation

Mirror facet degradation is a serious problem for high-power semiconductor lasers. It is a combination of COMD and chemical corrosion. COMD is considered one of the major limiting factors for achieving ultra-high optical power and is the main bottleneck limiting its reliability [27].

Figure 3 below illustrates the process of COMD generation, which is directly caused by high cavity surface temperatures. The relevant heating mechanism is the non-radiative recombination process induced by impurity oxides formed at the cleaved facet surface and the strong optical absorption process of the emitted laser light [28].



**Figure 2.** TEM image of dislocation dipole associated with <1 0 0> direction DLD in a rapidly degraded GaAlAs/GaAs double-heterojunction laser. Reprinted with permission from ref. [25] © Elsevier. Copyright 1999 Microelectronics Reliability.



Figure 3. COMD generation mechanism.

In addition, the type of package may lead to an increase in the cavity surface temperature: packages of semiconductor lasers usually have an "overhang" of about 10  $\mu$ m to protect the mirror facet's desorption from the solder material. As a result, the temperature of the mirror facet may be higher than that of the cavity [29]. The critical local temperature may be reached when the temperature is further increased by increasing the operating current or heat sink temperature, or when defects caused by aging are generated by long-term operation. At this point, intrinsic or extrinsic mechanisms leading to additional temperature increases from a positive feedback loop and thermal runaway start to occur.

When COMD occurs, DLDs are generated during the laser's operation; these DLDs are areas of non-radiative combination centers in the active region of the laser; they are generated locally, both on the cavity surface and inside the cavity, and can propagate along the cavity driven by the optical field, eventually leading to laser failure.

The photochemical action leads to slow oxidation of the mirror facet, which, in turn, leads to a gradual increase in local defects on the cavity surface and a change in their corresponding local reflection coefficients. An increase in optical absorption at the defects leads to an increase in local non-radiative recombination and a corresponding increase in temperature. Semiconductor lasers containing aluminum in the active region are more susceptible to catastrophic damage because aluminum adsorbs water and oxygen, making the laser cavity surface more susceptible to oxidation, which leads to the formation of local defects, and the device suffers catastrophic damage with increasing operation time.

# 2.2.3. Electrode Degradation

The main causes of electrode degradation are the degradation of ohmic contact and the degradation of thermal resistance. Ohmic contact degradation is mainly due to ion diffusion and electrical migration between the electrode material and the semiconductor material. In order to obtain a high output power, the electrodes of semiconductor lasers usually operate at very high current densities. At high current densities, electrical migration of metal ions in the electrodes occurs [30]. Metal ions undergo directional migration driven by high-density electron flow, and undergo significant mass transport, which, in turn, creates cavities voids or mounds (or whiskers) in the contact structures of the metal and semiconductor materials, producing electromigration failures. Electromigration, and thermal migration [31], leading to degradation failure of the electrode. In addition, impurity particles introduced during the welding process, as well as thermal stresses due to excessive temperature rising and falling, can also lead to degradation of the electrode.

# 2.2.4. Packaging-Related Degradation

The packaging stresses generated during the manufacturing process of a high-power semiconductor lasers can seriously affect their service life. Encapsulation stress is mainly caused by the mismatch of thermal expansion coefficients between the chip and the heat sink materials. Encapsulation stress changes the bandwidth of the semiconductor material and introduces a large number of dislocations in the active region, affecting the threshold current, wavelength, linewidth, and polarization of the laser [32]. Then, the service life of the laser is reduced, leading to transient failure. Solder (e.g., indium) creeps and climbs during operation, accumulating at the mirror facet and causing side leakage, leading to short circuits. Gaps created during the soldering process and oxidation of the solder can lead to laser degradation.

# 2.2.5. Influence of Environmental Factors

High-power semiconductor lasers are widely used in both terrestrial and space systems. For application scenarios in space systems, the exacerbating effect of radiation damage from the environment on laser degradation needs to be considered. Proton irradiation [33], electron irradiation [34], and  $\gamma$ -ray irradiation [35] can lead to crystal defects in semiconductor materials (e.g., Frenkel-type defects), which can affect the reliability of semiconductor lasers. The ambient temperature range for terrestrial systems is typically -40 to +85 °C. The temperature conditions in space are much harsher than those in terrestrial applications, e.g., from -120 °C to +120 °C. A drastic temperature variation range can also deteriorate the reliability.

The reliability of high-power semiconductor lasers can also be greatly affected by electrostatic discharge damage (ESD) generated during manufacturing, transportation, and use. ESD damage is typically caused by one of three events: direct static electricity from a person to a device, static electricity from a charged device, or field-induced discharge. Therefore, suitable models for simulating ESD-induced damage are called the Human Body Model (HBM), the Charged Device Model (CDM), and the Machine Model (MM), respectively. High-power semiconductor lasers are classified into edge-emitting lasers (EELs) and vertical cavity surface-emitting lasers (VCSELs) according to the emitting direction. VCSEL and VCSEL arrays are extremely sensitive to ESD [36,37]. If an ESD event occurs, catastrophic damage in the active area may occur leading to sudden failure of the semiconductor laser, and the laser may then degrade in its subsequent operation with the occurrence of other failure mechanisms, which can be easily confused with other causes of failure.

# 3. Accelerated Aging Test and Lifetime Test Method

### 3.1. Reliability

3.1.1. Reliability Overview

Reliability is the ability of a product to perform a "specified function" under "specified conditions" and for a "specified time". Reliability is usually reflected indirectly by the failure rate. According to Equation (1), failure of a semiconductor laser is generally defined as a failure when the drive current *I* remain constant and the output power  $P_{out}$  decreases to a certain percentage (e.g., 50% of the initial value, which can also be specified), or when the output power  $P_{out}$  remains constant and the drive current *I* rises to a certain degree (e.g., 120% of the initial value).

The failure rate of a semiconductor laser (instantaneous failure rate)  $\lambda(t)$  is the probability of failure per unit of time after the laser has been in operation up to the moment *t*.

Figure 4 shows a typical failure rate function for a 1550 nm distributed feedback (DFB) laser [38].





The failure pattern of most optoelectronic devices is similar to this curve and is divided into three phases called the early failure period, the random failure period, and the wear period.

- Early failure: The device has a high failure rate and a very short operating lifetime, usually due to rapid degradation caused by the rapid growth of the internal defects within the device. These defects are mainly generated during the manufacturing process.
- (2) Random failure: This stage has a low failure rate, is difficult or impossible to predict, and is associated with chance factors.
- (3) Wear and tear failure: The device shows wear and ages in different operating environments, reaching its service life, and eventually failing.

# 3.1.2. Reliability Experiments

Reliability experiments are an important method by which to study the reliability of high-power semiconductor lasers. Reliability experiments are divided into four main categories: environmental experiments, lifetime experiments, special test experiments, and field application experiments. The lifetime of a high-power semiconductor laser is an important indicator of its reliability. Lifetime experiments are common methods used to evaluate the reliability and degradation analysis of semiconductor lasers.

The lifetime experiments are divided into long-term lifetime experiments and accelerated aging experiments. With the development of high-power semiconductor lasers, their lifetimes have been continuously improved, and most of them have reached a lifetime on the order of 10,000 h. If the lifetime experiment is completed according to an actual application environment, the test time can be long and the device cost can be high. In order to save time and costs, accelerated aging experiments are often used to quickly determine the reliability of the device.

# 3.2. Accelerated Aging Experiment

# 3.2.1. Theoretical Basis for the Accelerated Aging Test

The idea of the accelerated aging experiment is to create an experimental environment at high stress levels, so the necessary experimental data of the test device can be obtained. Then, one can apply statistics or other data processing methods to extrapolate the lifetime characteristics of the device operating at normal stress [39]. Therefore, the relationship between lifetime characteristics and stress levels needs to be established. The common accelerated stresses used in aging experiments of high-power semiconductor lasers are mainly temperature stresses and electrical stresses.

# Arrhenius Model

An important model is the Arrhenius equation that expresses the temperature stress and reaction rate as follows [40]:

$$\frac{dM(T)}{dt} = A_0 exp\left(\frac{-E_a}{kT}\right) \tag{4}$$

 $\frac{dM(T)}{dt}$  is the rate of process to failure, indicating the speed of the reaction; *k* is the Boltzmann constant; *E*<sub>a</sub> is the activation energy that causes the failure or degradation process; *T* is the absolute temperature; and *A*<sub>0</sub> is a non-thermal constant factor. A cumulative failure distribution diagram at different constant temperatures is usually used to obtain the median life (mean time to failure) at each temperature. According to the Arrhenius curve of median life and temperature, the failure activation energy and the extrapolated working life of the devices can be obtained.

# Inverse Power Law Model

When the life of a system is an inverse power function of the accelerating stress variable, the inverse power law is commonly used. The inverse power law model describes the relationship between the applied stresses (e.g., voltage, current, optical power, temperature cycling, or mechanical vibration, etc.) and the lifetime of a semiconductor laser [41].

$$L_s = Av^{-C} \tag{5}$$

 $L_S$  represents the life at a stress of v; A is a constant typical for laser type; C is an exponent characteristic of the laser device, which is a positive constant related to the activation energy; and v is the accelerating stress.

Different stresses produce different failure mechanisms, and the choice of stresses in the experiment is determined by the actual situation. The Arrhenius model and the inverse power law model can be linearized and written uniformly in the following form:

$$L_s = a + b l n \phi(s) \tag{6}$$

 $L_S$  is the characteristic lifetime;  $\phi$  is a function related to the *s* stress; and *a* and *b* are coefficients that can be calculated from the experimental data.

### 3.2.2. Classification of Accelerated Aging Test

The accelerated aging test can be divided into constant stress, step stress, and sequential stress accelerated aging experiments according to the variation law of the accelerated stress applied in accelerated aging experiments [42].

Constant stress experiments are performed by dividing the stress levels into different groups and then testing the devices in different groups until all the devices fail. The stress level does not change during the whole experiment.

Step stress experiments are also divided into different stress levels. All the test devices are placed at the same level. After a period of time, the stress level is switched to a higher level. The failed devices are removed, then the experimental conditions are switched to another higher stress level, and so on, until a certain percentage of devices fail.

The sequential stress experiment is similar to the step stress experiment, except that the stress level of the sequential stress experiment increases continuously with time, which can also be regarded as the limiting case of the step stress, in which the time interval of the stress transition is considered to be very small.

Among these three experimental methods, the experimental environment setting of the constant stress experiment is relatively simple, but the test is very time-consuming; the experimental operation of the step stress experiment is more complicated than that of a constant stress experiment, but it is more time saving. The experimental environment setting of the sequential stress experiment is complicated, and there are fewer related reports.

### 3.3. Example of Accelerated Aging Test

Research on the lifetime of high-power semiconductor lasers around the world has focused on several large laser manufacturing companies and research institutions, such as IBM Zurich Research Laboratory [43,44], NASA [45,46], COHERENT [47], JOLD in Germany [48], the 13th Research Institute of China Electronics Technology Group Corporation [49], the Research Institutes of Chinese Academy of Sciences [50], *n*light Corporation [51,52], and the American Aerospace Corporation [53,54], etc.

In 1991, A. Moser and E.E. Latta of the IBM Research Division, Zurich Research Laboratory, determined the apparent Arrhenius parameters for the rate process of COMD failure. They compared the rate processes for various cleaved facets with and without a subsequent plasma oxidation step. A tentative model for facet heating that ultimately leads to COMD in AlGaAs/GaAs QW lasers was established [43].

In 1994, A. Oosenbrug and E.E. Latta of the IBM Research Division, Zurich Research Laboratory studied the high-power operational stability of 980 nm InGaAs/AlGaAs QW lasers. In total, 60 high-power semiconductor lasers were tested for their long-term lifetimes, with individual hours ranging from 6000 to 32,000 h, some at power levels above 200 mW (up to 300 mW continuous wave). For a group of devices operating at 200 mW continuous wave and at 50 °C, they found a log-normal distribution with a median life of >150 kh [44].

In 2005, Guoguang Lu et al. of Changchun Institute of Optics and Mechanics, Chinese Academy of Sciences, investigated the reliability of 808 nm high-power InGaAsP/InP lasers using constant stress accelerated aging tests [49]. They conducted constant current aging tests on six randomly selected lasers at 70 °C and 80 °C with an operating current of 1000 mA, and used the Arrhenius equation to derive a lifetime of 30,000 h at 25 °C.

In 2008, Hongde Wang et al. of the 13th Research Institute of China Electronics Technology Group Corporation conducted aging experiments on 808 nm AlGaInAs/AlGaAs/GaAs QW lasers [50]. They performed step stress experiments based on the inverse power law model, with electrical stress levels of 1, 1.3, 1.5, 1.7, and 2 A for a duration of 600 h. By comparing the failure rates of the devices with constant stress and step stress experiments, they found the same failure modes and similar lifetime estimates for both methods. This experiment verified that the step stress test is more time efficient than the constant stress test.

In 2011, *n*Light corporation performed reliability tests on 976 nm single-emitter laser diodes [51,52]. Seven sets of test conditions were performed: 18 A, 52 °C (drive current, junction temperature); 12 A, 62 °C; 16 A, 55 °C; 14 A, 58 °C; 15.7 A, 88 °C; 15 A, 73 °C; and 13.7 A, 42 °C. Over 15,000 h of accelerated life test reliability data were collected. The effects of temperature and power acceleration were evaluated by accelerated aging tests, and it was concluded that the mean time to failure was greater than 30 years at an output power of 10 W and a junction temperature of 353 K (80 °C), with a statistical confidence level of 90%.

In 2016, the American Aerospace Corporation reported their study of long-term accelerated aging tests on high-power, single-mode and multi-mode InGaAs/AlGaAs strained QW lasers [53,54]. They tested 64 single-mode 975 nm QW lasers with four experimental conditions: a drive current of 1.5 A and a junction temperature of 70 °C; a drive current of 1.5 A and a junction temperature of 120 °C; a drive current of 1.8 A and a junction temperature of 70 °C; a drive current of 2.1 A and a junction temperature of 70 °C. The test time for each test condition was 6500 h, and the test accumulation was 25,000 h (nearly 3 years). They tested 32 multi-mode lasers with lasing wavelengths of 920–960 nm at a drive current of 4 A and a junction temperature of 55 °C, for a total of over 35,000 h. This is the longest reported lifetime test for a single-mode or multi-mode laser in the 915–980 nm band. Their failure analysis of the devices showed that the main cause of failure was internal degradation.

In 2017, Zhiwen Wang et al. of the Institute of Semiconductors, Chinese Academy of Sciences, conducted a lifetime test of their self-developed 975 nm high-power semiconductor laser using a current step stress aging experiment [55]. They set the current stresses to 10, 12, and 14 A, and the aging times to 1200 h, 500 h, and 500 h. The test results were analyzed according to the inverse power law model, and the average lifetime of the device at a current of 8 A was calculated to be 28,999 h.

In 2019, constant temperature and constant current aging tests were conducted on 18 conduction-cooled packaged 60 W 808 nm high-power diode lasers by the Xi'an Institute of Optics and Precision Mechanics, Chinese Academy of Sciences, and Xi'an Focuslight Technologies Inc [56]. The average laser lifetime was 1022, 620, and 298 h at three different heat sink temperatures of 55, 65, and 80 °C with a constant current of 60 A. The lifetime at room temperature was calculated to be 5762 h according to the Arrhenius formula. The number of lasers in this experiment was relatively large, and the test temperature and output power were high. Because of the high costs and long duration of this experiment, such reports are quite rare.

### 4. Failure Analysis Techniques

The performance and reliability of semiconductor lasers can be significantly improved by precisely defining the location and cause of damage. Many techniques have been employed to characterize the failure modes and degradation mechanisms of high-power semiconductor lasers: the EBIC technique, the OBIC technique, the TIVA technique, EL,  $\mu$ -PL, EMMI, CL, ECCI, TEM, and Raman, etc.

# 4.1. EBIC

EBIC is a technique commonly used to detect defect localization in semiconductor lasers for in-depth failure analysis [57,58]. It is a scanning electron microscopy (SEM)-based technique used to measure the current flowing through a semiconductor. When an electron beam is shone on a semiconductor chip, electron–hole pairs are created in a certain range within the semiconductor. The induced current of carriers can be collected by the internal electric field, which detects electrical defects with reduced carriers due to recombination. The intensity of the EBIC signal corresponds to the strength of the internal electric field around the p–n junction. Defects that are non-radiative recombination centers show a significantly lower EBIC signal [59,60].

Since the depth of the generated carriers depends on the accelerating voltage, a wide voltage range from 5 to 40 kV is essential for the quantitative study of the defect activity. Typically, the lateral resolution of EBIC varies from 20 to 500 nm, depending on the SEM conditions and material composition.

In 2018, Yong Kun Sin et al. from the California Aerospace Corporation EI Segundo, employed the EBIC technique for the first time to determine the failure modes of 980 nm degraded single-mode InGaAs/AlGaAs strained QW lasers by observing DLDs [61]. Figure 5 shows the EBIC images of the high-power InGaAs/AlGaAs QW laser under different aging conditions: (a) 2.1 A/70 °C with a fail time of 10,205 h, (c) 1.8 A/70 °C with a fail

time of 2560 h, and (d) 2.1 A/70  $^{\circ}$ C with a fail time of 1180 h. This clearly shows that the onset of the DLDs is confined within the 4  $\mu$ m wide waveguide. The combination of the SEM and EBIC images indicates that the degradation process occurs in the active layer.



**Figure 5.** EBIC images were captured from high-power InGaAs/AlGaAs QW lasers with different aging conditions near the front facet. (a) 2.1 A/70 °C with a fail time of 10,205 h, (c) 1.8 A/70 °C with a fail time of 2560 h, and (e) 2.1 A/70 °C with a fail time of 1180 h and corresponding EBIC images near the rear facet (**b**,**d**,**f**). Reprinted with permission from ref. [61] © SPIE. Copyright 2018 Proceedings of SPIE.

In 2020, the rapid degradation behavior and failure mechanisms of InGaN/GaN green laser diodes (LDs) were investigated by the University of Shanghai for Science and Technology and the Chinese Academy of Sciences using the EBIC technique to assist in identifying the location of defects [62]. The SEM and EBIC images of the non-aged LD are shown in Figure 6a,d, and the SEM images of the two possible locations where the LD underwent severe degradation after the aging experiments are shown in Figure 6b,c. Compared with the non-aged LD, bubble-like defects can be observed in the images marked as P1 and P2, in which, combined with Figure 6e,f, the EBIC signal is slightly reduced. According to the EBIC principle, a higher carrier trap density exists in the active region of the laser near the bubble-like defects.

# 4.2. OBIC

OBIC is a non-destructive, highly sensitive, and high-resolution technique that is widely used to characterize defects present in semiconductor lasers such as stacking faults, dislocations, diffusion spikes, diffusion pipes, electrical over stress (EOS), and ESD damage. OBIC is a scanning optical microscopy imaging mode that locates regions of Fermi-level transitions. When the active region of a semiconductor laser is illuminated by a focused and scanned beam, the electron–hole pairs generated in the active region are separated by the built-in electric field in the p–n junction, and then collected by the electrodes to form a photocurrent that serves as the OBIC signal. Defects in the semiconductor material produce local variations in the Fermi level or the built-in potential that can enhance or weaken the recombination current, and, hence, the OBIC signal.



**Figure 6.** Typical secondary electron images of (**a**) non-aged LD and (**b**) and (**c**) rapidly degraded LD and the corresponding EBIC images of (**d**) non-aged LD and (**e**) and (**f**) rapidly degraded LD (the two black ovals mark two possible heavily degraded positions in the LD). Reprinted with permission from ref. [62] © Elsevier. Copyright 2020 Superlattices and Microstructures.

In 2007, Tatsuya Takeshita et al. of NTT, Japan, proposed a novel OBIC measurement technique to analyze the degradation position of a 2.5 Gbps directly modulated 1.55  $\mu$ m uncooled DFB laser [63]. Incident beam sources with wavelengths longer than the band edge of the InGaAsP active layer were applied to improve the sensitivity of the OBIC. They confirmed that the degradation mechanism of the 1.55  $\mu$ m InGaAsP/InP strained QW laser was mainly governed by diffusion defects on the waveguide rather than defects near the anti-reflective surface.

Figure 7a shows a digital OBIC scan image of the anti-reflective facet for the  $1.55 \,\mu$ m InGaAsP/InP strained QW laser [63]. It underwent an aging time of 9000 h at 95 °C. The bright areas in the figure represent the active region. To characterize the degradation of the semiconductor laser, the peak OBIC intensity of the laser was normalized to 1.0 before aging. Figure 7b shows the normalized OBIC (nor-OBIC) signal intensity profile of the laser perpendicular to the p–n junction before and after aging. The peak of the nor-OBIC intensity dropped to 0.95 after aging, which indicates the degradation of the active layer after aging.



**Figure 7.** OBIC technique for monitoring the internal degradation of the laser. (**a**) OBIC image of the laser and (**b**) normalized OBIC signal intensity profile. Reprinted with permission from ref. [63] © Elsevier. Copyright 2007 Microelectronics Reliability.

In 2021, Che Lun Hsu et al. from National Yang Ming Chiao Tung University presented a modified OBIC microscope based on a tunable ultrafast laser to spectrally resolve the failure point of an electrostatic discharge-damaged VCSEL [64]. A spectrally resolved OBIC image of a normal VCSEL is shown in Figure 8 [64].





The OBIC image was obtained by adjusting the incident wavelength at 5 nm intervals between 780 and 900 nm. The photocurrent signal was adjusted by a voltage preamplifier before being fed to the A/D converter for mapping. The obtained OBIC images are presented in a color-coded format. The active region of the OBIC confined by the oxide layer can be identified, resulting in a circular pattern. In a normal VCSEL (Figure 8a), the photocurrent distribution in the active region is very uniform. The maximum intensity of the photocurrent appears when the incident wavelength is 780 nm. When the incident wavelength is longer than 860 nm, there is no photocurrent response.

Figure 8b shows the spectrally resolved OBIC image of the ESD-HBM (human body model) VCSEL, in which it can be seen that there are two defect points in the active region: a large one at the edge and a small one near the center of the edge. Therefore, the OBIC technique can precisely define the damage location and failure cause of ESD-damaged VCSELs.

# 4.3. TIVA

Due to it being non-invasive and having a simple sample preparation, the TIVA technique is widely recognized as a fast and effective tool for locating defects in current biased devices, especially those with poor optical emission [65,66]. It is a type of thermal laser stimulation (TLS) technique. A focused 1.064  $\mu$ m laser beam is used as an active probe to scan and locate defects in group III–V semiconductor lasers. The focused laser scan causes a localized change in thermal gradient at the scan location, which results in a change in local resistance. The change in resistance is captured when monitoring the voltage change across a fixed current source.

TIVA has been widely used to perform topside and backside inspections of failed VCSELs. Robert W. Herrick et al. of the Intel Corporation used the "backside TIVA" technique to inspect the DLDs in VCSELs [67]. Figure 9 shows TIVA images of mesa oxide in VCSELs before and after aging tests [67]. The reflected bright image (Figure 9a) shows a mesa with a diameter of about 42  $\mu$ m and an oxide pore size of 12  $\mu$ m. Figure 9b shows the TIVA image of the back side of the device under normal operation conditions. It shows some evidence of damage in the upper left corner, but it has not yet spread. The TIVA image of the device (Figure 9c) shows the DLD propagating from the edge of the mesa in the lower right corner and then causing a failure as it reaches the emitting region in its center.



**Figure 9.** TIVA for detecting DLDs in VCSEL. (**a**) Reflected light image, (**b**) TIVA image of a normal VCSEL, and (**c**) TIVA image of a VCSEL damaged by aging test. Reprinted with permission from ref. [67] © Springer Nature. Copyright 2013 Springer eBook.

# 4.4. EL

EL is always employed to detect hidden cracks, black blocks, dislocations, and stacking faults in semiconductor lasers. The use of EL is usually sufficient to classify the location of the damage in EELs. When a fixed field is applied to the active region of a laser chip, the part without defects emits photons due to electron and hole recombination, while the defective part without electron and hole recombination shows a dark region. EL imaging is particularly suited to study the development of DLDs caused by luminescence-killing dislocation networks in laser cavities. EL line scans across a defect structure provide useful quantitative information.

In 2018, Yongkun Sin et al. from the Aerospace Corporation, El Segundo, found a new failure mode in high-power multi-mode InGaAs/AlGaAs strained QW lasers using EL techniques for short-term step stress tests and long-term accelerated aging tests [68]. Figure 10 shows the top surface EL images of two multi-mode InGaAs/AlGaAs strained QW lasers [68]. Figure 10a shows the DLD starting from the front mirror, indicating the degradation of the facets (COMD failure). Additionally, the DLDs of the bulk failure in Figure 10b start from the inside of the cavity, indicating catastrophic optical bulk damage (COBD failure).





**Figure 10.** EL images of degraded multi-mode lasers show a facet failure and a bulk failure. Reprinted with permission from ref. [68] © Springer Nature. Copyright 2018 MRS Advances.

# 4.5. *μ*-*PL Mapping*

The  $\mu$ -PL mapping technique is a simple, non-destructive, and non-contact method used to detect the defect concentrations and crystalline quality of semiconductor materials. The semiconductor material is irradiated by a certain frequency of light. The electrons in the defect-free parts are excited to a higher electronically excited state, and then radiate a photon as the electrons return to a lower energy state and release energy (photons). PL intensity mapping and peak wavelength mapping in semiconductor wafers as grown and processed can provide information on material uniformity, dislocation density, the density of deep traps, the distribution of residual impurities, and anneal uniformity.

In 2006, Marwan Bou Sanayeh et al. of OSRAM Opto Semiconductor GmbH employed the  $\mu$ -PL mapping technique to investigate COMD-induced defects in high-power AlGaInP broad-area lasers [69]. Figure 11 shows the  $\mu$ -PL mapping of the COMD laser. From the magnification of the defects near the output facet, highly non-radiative defects start from the output facet and propagate parallel to the waveguide direction inside the cavity.



**Figure 11.**  $\mu$ -PL mapping of a laser after COMD with a magnification of the output facet (white = low PL intensity and black = high PL intensity). Reprinted with permission from ref. [69] © AIP Publishing. Copyright 2006 Applied Physics Letters.

# 4.6. EMMI

The EMMI technique is a highly efficient and high-precision, non-destructive method used to detect leakage currents from device defects, ESD failures, junction leakage, contact

spiking, and hot electrons, etc. By detecting the photons excited by the recombination of electron and hole, the failure position and mechanism can be deduced. Based on the precise positioning of the EMMI, focused ion beam (FIB) slitting enables direct observation and analysis of the localization of failure points.

In 2021, Roert Fabbro et al. from AMS, Unterpremsteatten, AG, Austria, and Graz University of Technology, Graz, applied the reverse-bias EMMI technique to detect defect localization in high-power VCSEL arrays. They performed failure detection on a high-power 2D-VCSEL array consisting of 932 emitters, as shown in Figure 12a [70].



**Figure 12.** (a) High-power 2D-VCSEL array consisting of 932 emitters. (b) Reverse-bias EMMI image of region A of the VCSEL array before stress; (c) reverse-bias EMMI image of region A of the VCSEL array after stress; (d) reverse-bias EMMI image of region B in the VCSEL array after stress; and (e) colorized close-up of the marked emitter in (d). Reprinted with permission from ref. [70] © IOP Publishing. Copyright 2021 Measurement Science and Technology.

Figure 12b,c show the photon emission changes of individual emitters on the VCSEL array in a reverse-bias EMMI before and after stress [70]. They found that the most promising emitters showing degradation and defects were those showing a change from dark to bright (i.e., the change shown between Figure 12b,c) The emission intensity of each emitter depends on the intrinsic shunt resistance; defects such as cracks, ESD damage, and DLDs also act as leakage current pathways by damaging the internal crystal structure. These additional pathways increase the overall leakage current flowing through the emitters during PN junction reverse bias, and avalanche breakdown occurs, thus increasing the emission intensity of the damaged emitters during reverse bias. Figure 12d presents the reverse-bias EMMI image of region B in the VCSEL array after stress, where one of the emitters has high brightness (red square on the right side of the figure). A colorized close-up of the reverse-bias emission pattern of one of the emitters with a high brightness shows emissions across the whole emitter with two main defect spots (Figure 12e).

# 4.7. CL

The cathodoluminescence (CL) spectroscopy technique has a nanoscale spatial resolution (30–50 nm), which is normally applied to characterize the changes in composition,

strains, and crystalline defects in semiconductor materials, such as dislocations, stacking faults, and impurities, etc. It can track elements and impurities and analyze the failure causes of semiconductor materials and defect localization within lasers. The CL technique is a powerful tool for observing dislocation defects in the active region of a semiconductor laser.

Due to the interaction of high-energy electrons (cathode rays) with a semiconductor material, electromagnetic radiation or a light ranging from visible to near-infrared is created by electron transitions in bandgaps or defect localizations. Using a specialized SEM, the electromagnetic radiation or light can be collected.

In 2013, V. Hortelano from the University of Valladolid investigated the defect signatures in degraded high-power laser diodes [18]. The degradation mechanism of an aged 980 nm single-ridge waveguide InGaAs/AlGaAs-based QW laser and broad-area emitter 808 nm AlGaAs/GaAs QW laser were studied using the CL technique. The characteristics are shown in Figure 13. Figure 13a,b show the CL images of DLDs in the 980 nm InGaAs/AlGaAs QW laser. The <100> DLDs at 45° to the cavity and the <1–10> DLDs perpendicular to the cavity can be observed in Figure 13 (a) and (b), respectively. The propagation of the DLD in Figure 13a is driven by the REDC mechanism. The propagation of the DLD in Figure 13b is driven by the REDG mechanism. Figure 13c shows the V-shaped defect in an aged 808 nm AlGaAs/GaAs QW laser.



**Figure 13.** DLDs observed by the CL technique. (a) CL image of DLDs at  $45^{\circ}$  to the cavity, (b) CL image of DLDs perpendicular to the cavity, and (c) CL image of V-shaped defects. Reprinted with permission from ref. [18] © Elsevier. Copyright 2013 Microelectronics Reliability.

# 4.8. TEM

TEM is based on the interaction between a high-energy electron beam and a very thin sample. It is a high-resolution technique used to reveal the crystallographic phase at the nanometer scale; to characterize crystal defects such as dislocation, grain boundaries, voids, and stacking faults in a III–V super lattice; and to identify nanometer-sized defects on integrated circuits, including embedded particles and via residues.

In 2010, Shigetaka Tomiya et al. from Advanced Materials Laboratories, Sony Corporation, investigated the structural defects and degradation phenomena in high-power pure-blue InGaN-based laser diodes. They proposed that the 440–450 nm InGaN-based LDs were degraded by an increase in the capture cross-section of non-radiative recombination centers at the active regions due to the diffusion of point defects and/or impurities during laser operation [17].

They confirmed that the introduction of a current injection-free region at/near the laser facet had a significant effect on the suppression of COMD degradation. Additionally, the increase in non-radiative recombination centers was caused by the current injection and not by an optical effect. The multiple defects consisted of a combination of columnar defects, and dislocations were demonstrated by cross-sectional TEM images of the multiple quantum wells (MQWs).

# 4.9. ECCI

ECCI relies on electron diffraction inside crystalline materials. The information of the crystalline defect is carried by elastically backscattered electrons, which can be significantly modulated by the crystal orientation near the top surface. It is a helpful method used to examine the dislocation distribution and cross-sectional facet conditions of aged devices. It allows the detection and characterization of dislocation substructures near the surface of bulk specimens, which can be retested and re-examined, and it allow a large area of the specimen to be examined, thus allowing statistical information to be collected for large numbers of persistent slip bands and cracks.

The advantage of adopting the ECCI method compared to cross-sectional TEM image is that no sample preparation is required, and, moreover, one can continuously monitor the device aging process without damaging the device structure.

In 2021, Bei Shi et al. from the University of California Santa Barbara, USA, applied the ECCI technique to examine the dislocation distribution of aged 1550 nm InGaAs/InP QW laser diodes monolithically grown on silicon [20]. A high density of V-shaped defects originating from the multiple-QW region was detected before device aging. Extended DLDs were observed in the QW region, the InP buffer, and the strained-layer super lattices. They found that the DLDs introduced new misfit dislocations and promoted the climbing of threading dislocations, leading to device degradation.

### 4.10. Raman

Laser Raman microprobe spectroscopy is a non-destructive detection method that measures damaged portions of semiconductor materials and characterizes the compositional changes and lattice quality of the epitaxial layers [71]. It is an analytical technique in which scattered light is used to measure the vibrational energy pattern of the sample. The stresses and strains present in the sample can be obtained by comparing the changes in the peak positions of the Raman peaks; the polarization direction of Raman is used to determine the crystal symmetry and lattice orientation. By comparing the intensity of the Raman peaks, the total amount of substances in the material can also be known.

In addition to the above methods, there are many other techniques that can be applied to analyze the degradation mechanism of high-power semiconductor lasers, such as deep level transient spectroscopy (DLTS) [72,73], X-ray diffraction (XRD) [74,75], scanning electron microscope (SEM) [76], secondary ion mass spectroscopy (SIMS) [77,78], X-ray photoelectron spectroscopy (XPS) [79], and infrared thermal image detection technology (IRT) [80]. The principles of the different detection techniques are quite different, so there are big differences in the difficulty of the detection techniques, sample preparation methods, and matters needing attention.

Table 2 lists the detection category, selectivity, and technical advantages and limitations of different detection techniques. In practical applications, the advantages and strategies of the various techniques should be comprehensively considered to find the root cause of the device's failure.

Method	Detection category	Selectivity	Advantages	Limitation
EBIC	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside and front facet) Destructive (EEL, backside)	High	Detecting the DLDs, stacking faults and precipitates in the active region nearby	Defects in cladding or contact layer are not visible
OBIC	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside and front facet) Destructive (EEL, backside)	High	Detecting degradation area and degree in the buried heterojunction and waveguide layers	For high-speed devices, the ability to observe the spectral response adequately is limited

Table 2. Comparison of various detection methods.

Method	Detection category	Selectivity	Advantages	Limitation
TIVA	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside)	High	Sensitive to leakage current and can detect the failure both on the surface and below	Signal might be blocked by metallic layers
EL	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside and front facet) Destructive (EEL, backside)	Medium	Good at detecting the development of DLDs caused by luminescence-killing dislocation networks in the laser cavity	Not suited to detecting lasers with blanket metal over the epi-side, or mounted epi-side-down
µ-PL	Non-destructive (epitaxial wafer, topside) Non-destructive (VCSEL, topside) Non-destructive (EEL, topside) Destructive (EEL, backside)	High	Measures isolated defects and residual mechanical stress induced by the packaging process	Cannot provide information of luminescence features with dimensions below the classical diffraction limit
CL	Non-destructive (epitaxial wafer, topside) Non-destructive (VCSEL, topside) Non-destructive (EEL, topside) Destructive (EEL, backside)	High	Sensitive to the presence of non-recombination centers, high lateral and in-depth resolution	Result depends greatly on the device resistances
EMMI	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside and front facet)	Medium	The luminescent defects can be located, low leakage current requirement	Poor resolution of non-luminous defects in metallic shaded areas
DLTS	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside)	High	Sensitive to deep level defects, comprehensive characterization information	Cannot obtain deep energy level electron wave functions or defect internal information
Raman	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside)	Medium	No need for sample processing, short time, high sensitivity	Weak signal strength
FIB-TEM	Destructive (VCSEL, topside) Destructive (EEL, topside)	High	Maximum magnification, high image quality, deep failure analysis; root cause identification	Significant sample preparation time, small sampling volumes, unable to detect the materials' instability in high-energy electron beams
XRD	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside)	High	Small sample size and simple interpretation result	Susceptible to interference of mutual elements and superposition peaks
SEM	Non-destructive (epitaxial wafer, VCSEL, EEL, topside) Non-destructive (epitaxial wafer, cross-section) Non-destructive (EEL, front facet) Destructive (EEL, backside)	High	Detects failures such as delamination, oxidation contamination, and melting at the surface	Cannot detect root cause hidden below surface

# 21 of 26

Method	Detection category	Selectivity	Advantages	Limitation
ECCI	Non-destructive (epitaxial wafer, topside) Non-destructive (VCSEL, topside) Non-destructive (EEL, topside) Destructive (EEL, backside)	High	High precision, no need for sample preparation, allows the examination of large area of specimen	Resolution is limited by SEM system; the background signal is strong; and dislocation detection and signal analysis are prone to interference
SIMS	Destructive (epitaxial wafer, VCSEL, EEL, topside)	High	Can acquire the information of several atomic layers or even single elements	Difficulties in quantitative analysis
XPS	Non-destructive (VCSEL, topside) Non-destructive (EEL, topside)	High	Good repeatability of spectral line, fast speed and high sensitivity	Low energy resolution and low peak-to-back ratio
IRT	Non-destructive (VCSEL, topside) Non-destructive (EEL, front facet) Destructive (EEL, topside)	Medium	Strong signal and easy to measure	Large quantitative analysis errors and low sensitivity

# Table 2. Cont.

# 5. Failure Improvement Measures

By analyzing the failure modes and causes of high-power semiconductor lasers, methods by which to improve the reliability of high-power semiconductor lasers are proposed in terms of the preparation process, reliability screening, and method application.

# 5.1. Preparation Process

To avoid internal degradation, it is very important to avoid defects caused by epitaxial growth and to minimize internal and external stresses in semiconductor lasers.

Substrates with low or no dislocation density are commercially available to avoid the propagation of threading dislocations into the active region. Designing an appropriate buffer layer or an etching stop layer can reduce the defects caused by the segregation process between atomic layers. Minimizing the oxygen content in the epitaxial growth atmosphere and selecting lattice-matched epitaxial films are useful methods by which to reduce the internal stresses and defects in laser diodes.

To reduce the external stresses in the bonding process, a low-melting-point Aurumstannum (Au–Sn) alloy solder with strong oxidation, good thermal conductivity, and less susceptibility to metallurgical reactions can be used to replace the original Indium (In) solder. Appropriate composite heat sinks with high thermal conductivity and less thermal expansion stress can effectively improve the reliability of semiconductor lasers due to their good match with the thermal expansion efficiency of the chip. Appropriate designs of the high heat flux microchannel heat sink exchanger can be used for the cooling of semiconductor laser diode arrays. High-efficiency coupling and package technique are necessary to increase the lifetime of laser diodes.

The application of effective surface passivation; vacuum cleavage and coating techniques; or the fabrication of non-absorbing mirror structures, such as the use of QW intermixing techniques, can be used to prevent the COMD phenomenon.

# 5.2. Reliability Screening

High-power semiconductor lasers are generally required to operate for long periods with continuous electricity input and are subject to changes in environmental conditions (radiation, temperature, and humidity, etc.), and therefore require a high degree of reliability and stability. To ensure the quality of the devices in the application system, semiconductor lasers must undergo rigorous inspection and aging screening prior to assembly. In the manufacturing process of lasers, early failures are usually accelerated by aging screening to screen out early failing devices and to ensure the reliability of semiconductor lasers.

Aging screening refers to the method of applying stress during the early operation of the device to accelerate the discovery of early failing devices and to screen out potential faults in time [81]. The applied stress can be thermal stress, electrical stress, mechanical stress, or a combination of different stresses. The applied stress should be controlled within an appropriate range. If it is too small, it may not achieve the screening effect, and if it is too large, it will introduce new failure causes. At present, the main methods used are high-temperature storage, high- and low-temperature shock, high-temperature power aging, mechanical vibration, centrifuge acceleration, leakage, and humidity and heat, etc., among which high-temperature power aging is the most common method [82].

The benefit of aging screening is that it can greatly reduce the risk of early failure, but it can also increase the overall production costs. For example, for a 144-pin package product, 24-h aging is used, and the cost is about 30% of the total cost [83].

Typical failure criteria of pump semiconductor lasers include, for example, a 5% reduction in ex-facet power during accelerated life tests at high temperatures and power or a 50% increase in the threshold current at the rated power [1].

# 5.2.1. High-Temperature Storage

High-temperature storage is a static aging mode. The devices are aged and screened after being placed in a high-temperature environment for certain time. The method mainly examines the effect of temperature on the device, which can accelerate chemical reactions on the chip's surface, the extension of defects inside the device, and the oxidation process of virtual solder joint, etc. [84]. The influences of high temperatures on the performance of the device include electrical parameter changes, heat dissipation difficulties, thermal aging, and chemical decomposition.

The devices are subjected to environmental tests of high-temperature storage between 120 °C to 300 °C; the storage time can be from dozens of hours to hundreds of hours. Generally speaking, the higher the temperature and the longer the storage time, the better the effect. The test depends on the reliability requirements of the device.

### 5.2.2. High- and Low-Temperature Shock

High- and low-temperature shock means that the device is stored in alternating highand low-temperature environments without powering up the device. It is a useful way to check the match conditions of thermal expansion coefficients between different epitaxial materials. Potential quality-related problems (cracks, sealing performance, and pressure welding) can be found.

The rigor of the test depends on the selected high and low temperatures, the length of exposure time, and the number of cycles.

# 5.2.3. High-Temperature Power Aging

High-temperature power aging refers to a certain high-temperature stress and electrical stress on the device. It is an effective aging method used to simulate the operating conditions of the device in an actual circuit, coupled with high-temperature (+80  $^{\circ}$ C–180  $^{\circ}$ C) aging.

High-temperature power aging is suitable for filtering semiconductor lasers with surface contamination, wire welding, current leakage, cracks, oxide layer defects, and local hot spots, etc.

# 5.3. Method Application

When the semiconductor laser is in operation for a long time, electrical or voltage overload, electrical surges, and thermal overload often occur. These phenomena could be

decreased, such as intentionally reducing the electrical stress, thermal stress, or mechanical stress in the laser.

High-power semiconductor lasers must have good thermal resistance, and the internal and external thermal resistance of the device should be reduced as much as possible. Scratches and mechanical damage should be prevented during the operation and use of the laser diode.

It is important to minimize the generation of static electricity, for example, by designing the physical circuit of the electrostatic discharge outside the laser; by using an anti-static carpet, workbench, and clothing; or by controlling the humidity of the environment. The welding and testing instruments must be properly grounded.

### 6. Conclusions

This paper introduces the failure mechanisms, accelerated aging experiments, failure detection techniques, and improvement methods of high-power semiconductor lasers. It can be seen that the reliability of high-power semiconductor lasers is influenced by many factors, and the reliability can be improved by using a comprehensive analysis of multiple techniques. Choosing the right experimental aging conditions not only saves time but also reveals the correct failure mechanism in semiconductor lasers and obtains the true reliability level of the devices. Choosing the proper failure detection techniques helps one to correctly analyze the causes of failure in high-power semiconductor lasers so as to improve their lifetime and reliability.

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